**Swinburne University of Technology**

# ***School of Software and Electrical Engineering***

**Contribution Document**

Subject Code: SWE30003 Unit Title: Software Architectures and Design

Assignment number and title: 2, Object Design Due date: 17th March 2024

Tutorial Day and Time: Tuesday 9:00AM Project Group: 9

Tutor: Mandeep Dhindsa

1. **Time spent** (*hours*): fill in the time spent on each significant work item in the following table.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Work Item** | **Tran Quoc Dung** | **Pham Hoang Duong** | **Nguyen Thai Son** | **Nguyen Manh Duc** |
| Executive Summary |  | 1 hour |  |  |
| Project Goals & Simplifications | 2 hours |  |  |  |
| Project Assumptions |  | 1 hour |  |  |
| CRC Cards | 2 hours | 1 hour | 3 hours | 2 hours |
| Bootstrap Process | 10 hours | | | |
| Design Heuristics | 1 hour |  | A tutorial session (3 hours) | |
| Design Patterns | 1 hour |  | 4 hours | |
| Verifications | 4 hours | 3 hours | 5 hours |  |
| Total | 20 hours | 16 hours | 20 hours | 19 hours |

**Description of contributions:**

***Tran Quoc Dung***: Project Goal, Project Simplification, UML, Design Heuristics, CRC Cards, Bootstrap Process, Verifications

* Dividing the work and figuring how the requirements specification will be developed.
* Team Leader

***Pham Hoang Duong***: Executive Summary, Project Assumptions, CRC Cards, Bootstrap Process, Verifications

***Nguyen Thai Son***: UML, CRC Cards, Design Heuristics, Design Patterns, Verifications

***Nguyen Manh Duc***: UML, CRC Cards, Design Heuristics, Design Patterns, Bootstrap Process

1. **Evidence of collaboration / teamwork on each significant work item (eg, discussions, mutual revisions and reviews, etc.):**

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A computer screen shot of a diagram

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1. **Signed by all members:**

ID Number Name Signature

|  |  |  |
| --- | --- | --- |
| 103803891 | Tran Quoc Dung | QDung |
| 103843282 | Pham Hoang Duong | HDuong |
| 103806531 | Nguyen Thai Son | ThSon |
| 103792724 | Nguyen Manh Duc | ManhDuc |